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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/820,348	04/07/2004	Roberto Aiello	012DIV-124	8143
44279	7590	01/05/2006	EXAMINER	
PULSE-LINK, INC. 1969 KELLOGG AVENUE CARLSBAD, CA 92008			AHN, SAM K	
			ART UNIT	PAPER NUMBER
			2637	

DATE MAILED: 01/05/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/820,348	Applicant(s) AIELLO ET AL.	
	Examiner Sam K. Ahn	Art Unit 2637	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 19 October 2005.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1,3-7,9 and 10 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1,3-7,9 and 10 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 07 April 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Response to Arguments

1. Applicant's arguments filed 10/19/05 have been fully considered but they are not persuasive. Applicants have amended the claims to include the limitation of "the slot allocation unit capable of different repetition frequencies" and cites the teaching of Dress (col.8, lines 59-61) of providing a stable, single frequency. The examiner respectfully disagrees. The stable, single frequency mentioned by the applicants is provided by the master clock oscillator (1001 in Fig.10, note col.8, lines 57-61), however, this is further provided to the programmable delays (1005). Dress further teaches wherein the at least one slot allocation unit is capable of different repetition frequencies (1005 coupled to 1003 then to 1004 providing 7 derivative pulses individually, the programmable delays receive stable single frequency clock and provides seven derivative pulses having different repetition frequencies controlled by the programmable delay, note col.8, lines 61- col.9, line 4). Therefore, Dress teaches all the limitations claimed.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States

only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1,3-7,9 and 10 are rejected under 35 U.S.C. 102(e) as being anticipated by Dress, Jr. et al. USP 6,603,818 B1 (Dress).

Regarding claims 1 and 6, Dress discloses a transmitter MAC layer (see Fig.10) comprising a clock synchronization unit (1001,1002) having a timing device with a clock speed, at least one frequency divider (1005,1003) coupled to said clock synchronization unit, said frequency divider configured to reduce said clock speed to generate a desired pulse repetition frequency (note col.8, line 43 – col.9, line 4), at least one slot allocation unit (1004) coupled to said at least one frequency divider, and a multiplexer/demultiplexer (1008,1009) operatively coupled to said at least one slot allocation unit, said multiplexer/demultiplexer configured to merge a plurality of outgoing signals (note col.9, lines 47-56).

It is well-known in the art that the function of the frequency divider (1005,1003) delaying of a master clock to produce a delayed clock is equivalent to the function of “reducing a clock speed” to produce a desired clock. Dress teaches a frequency divider by the implementation of a programmable delay (1240) and pseudorandom polynomial generator (1220, note col.10, lines 51-55). Thus, Dress teaches the frequency divider (1005,1003) configured to reduce said clock speed to generate a desired pulse repetition frequency.

Dress further teaches wherein the at least one slot allocation unit is capable of different repetition frequencies (1005 coupled to 1003 then to 1004 providing 7 derivative pulses individually, the programmable delay receives stable single

frequency clock and provides seven derivative pulses having different repetition frequencies controlled by the programmable delay).

Regarding claims 3-5 and 9-10, Dress teaches all subject matter claimed, as applied to claim 1 or 6. Dress further teaches wherein each of said at least one slot allocation unit is configured to support different modulation techniques (note col.9, lines 17-46).

Regarding claim 7, Dress teaches all subject matter claimed, as applied to claim 6. Dress further teaches wherein each of said at least one slot allocation unit has a particular pulse repetition frequency (note col.8, line 57 – col.9, line 16).

Conclusion

3. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory

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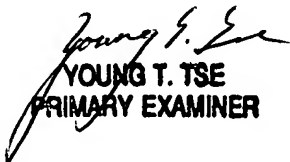
period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Sam Ahn whose telephone number is (571) 272-3044. The examiner can normally be reached on Monday-Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jay Patel can be reached on (571) 272-2988. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Sam K. Ahn
12/23/05


YOUNG T. TSE
PRIMARY EXAMINER